

2SD882S

NPN Silicon Epitaxial Planar Transistor

for the output stage of 0.75 W audio, voltage regulator, and relay driver.

The transistor is subdivided into three groups Q, P and E, according to its DC current gain.

On special request, these transistors can be manufactured in different pin configurations.



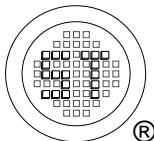
1. Emitter 2. Collector 3. Base
TO-92 Plastic Package

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Collector to Base Voltage	V_{CBO}	40	V
Collector to Emitter Voltage	V_{CEO}	30	V
Emitter to Base Voltage	V_{EBO}	5	V
Collector Current	I_C	3	A
Power Dissipation	P_{tot}	750	mW
Junction Temperature	T_j	150	°C
Storage Temperature Range	T_{stg}	- 55 to + 150	°C

Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Unit
DC Current Gain at $V_{CE} = 2 \text{ V}$, $I_C = 1 \text{ A}$ Current Gain Group Q P E at $V_{CE} = 2 \text{ V}$, $I_C = 20 \text{ mA}$	h_{FE}	100	-	200	-
	h_{FE}	160	-	320	-
	h_{FE}	200	-	400	-
	h_{FE}	30	-	-	-
Collector Base Cutoff Current at $V_{CB} = 30 \text{ V}$	I_{CBO}	-	-	1	µA
Emitter Base Cutoff Current at $V_{EB} = 3 \text{ V}$	I_{EBO}	-	-	1	µA
Collector Base Breakdown Voltage at $I_C = 100 \mu\text{A}$	$V_{(BR)CBO}$	40	-	-	V
Collector Emitter Breakdown Voltage at $I_C = 1 \text{ mA}$	$V_{(BR)CEO}$	30	-	-	V
Emitter Base Breakdown Voltage at $I_E = 10 \mu\text{A}$	$V_{(BR)EBO}$	5	-	-	V
Collector Emitter Saturation Voltage at $I_C = 2 \text{ A}$, $I_B = 200 \text{ mA}$	$V_{CE(sat)}$	-	-	0.5	V
Base Emitter Saturation Voltage at $I_C = 2 \text{ A}$, $I_B = 200 \text{ mA}$	$V_{BE(sat)}$	-	-	2	V
Transition Frequency at $V_{CE} = 5 \text{ V}$, $I_C = 0.1 \text{ A}$, $f = 100 \text{ MHz}$	f_T	-	90	-	MHz
Collector Output Capacitance at $V_{CB} = 10 \text{ V}$, $f = 1 \text{ MHz}$	C_{ob}	-	45	-	pF



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Dated: 22/02/2016 CL Rev: 02